

FIG. 1 is a block diagram of a system architecture.

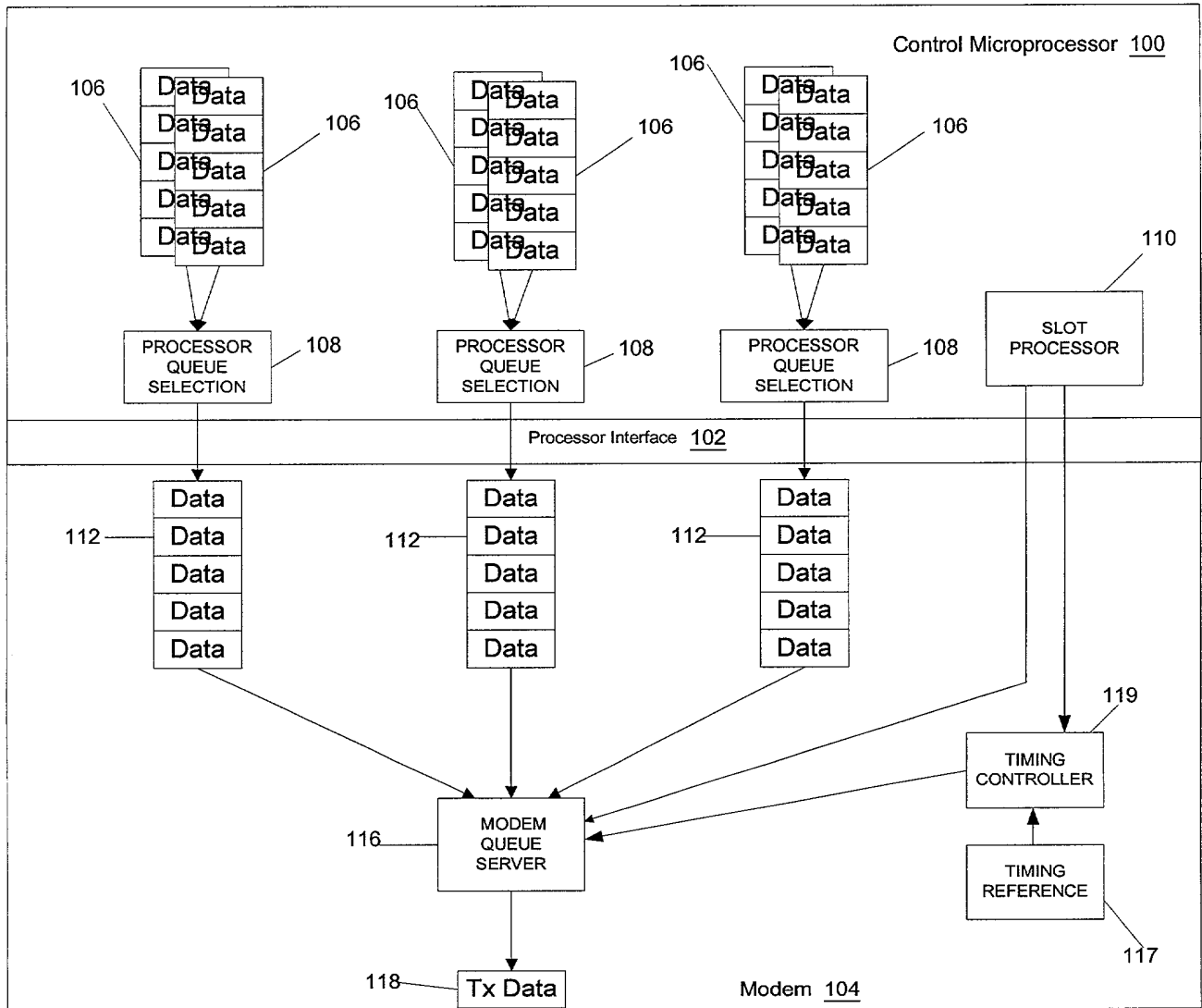


Figure 1

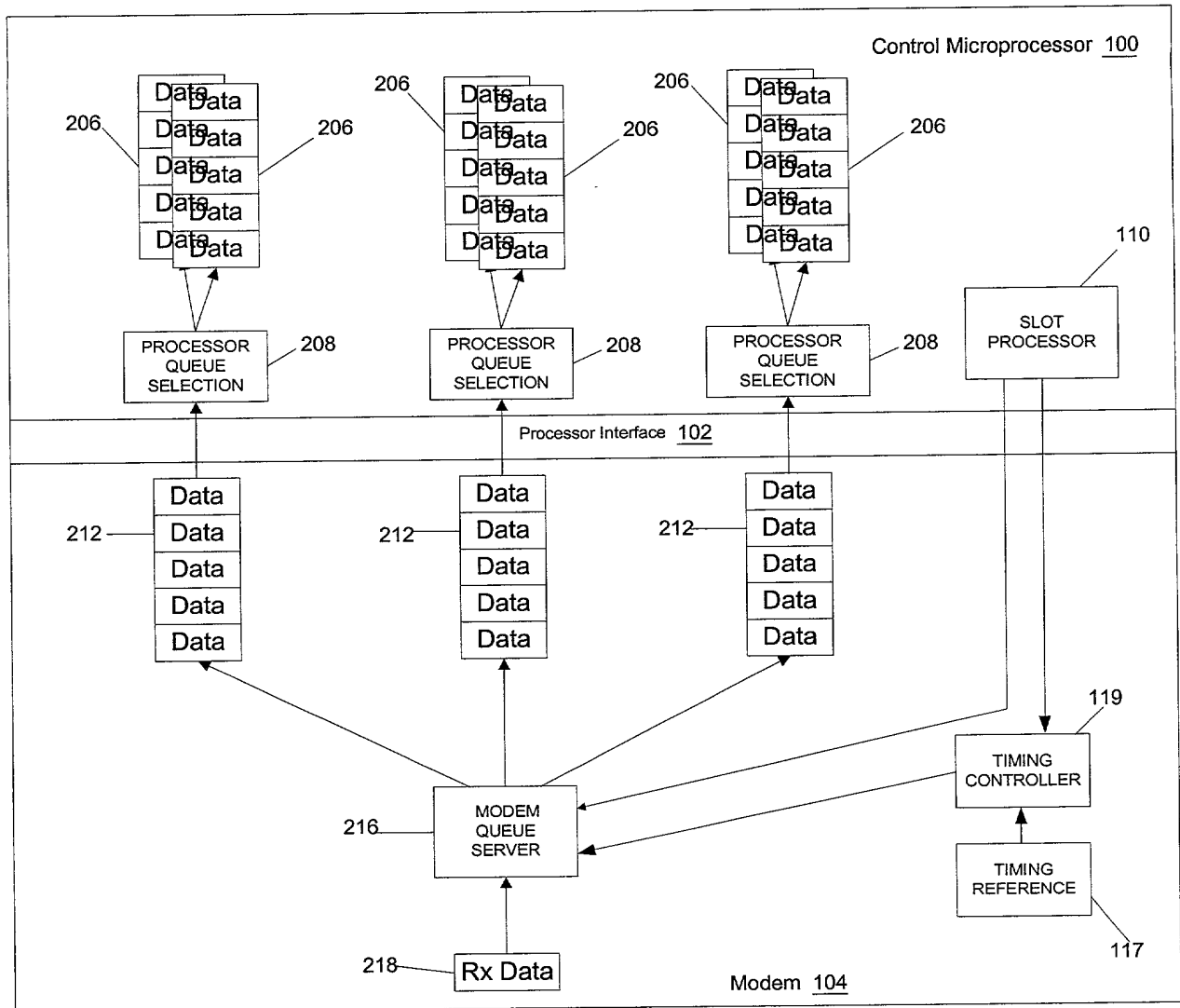


Figure 2



Figure 3

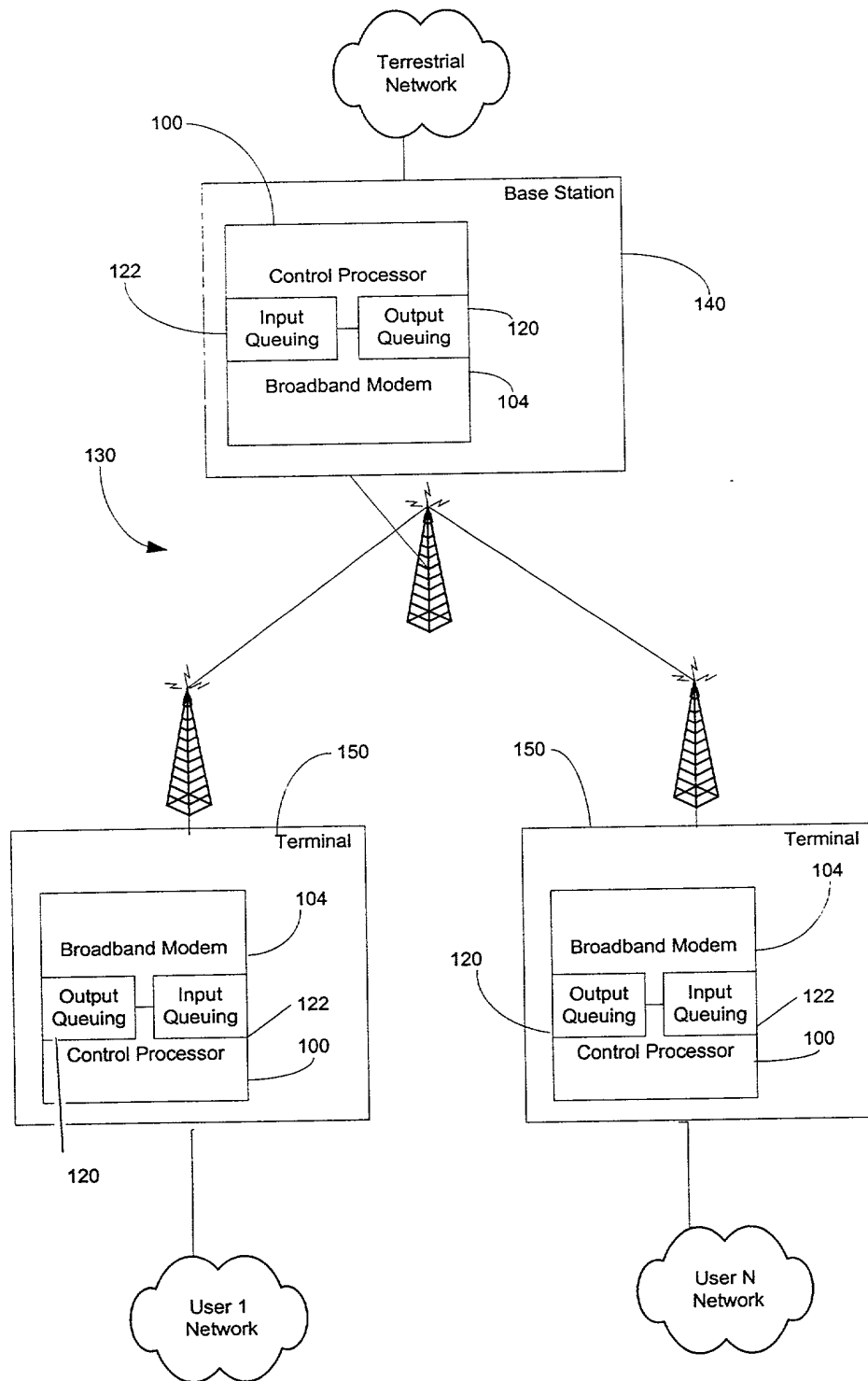


Figure 4

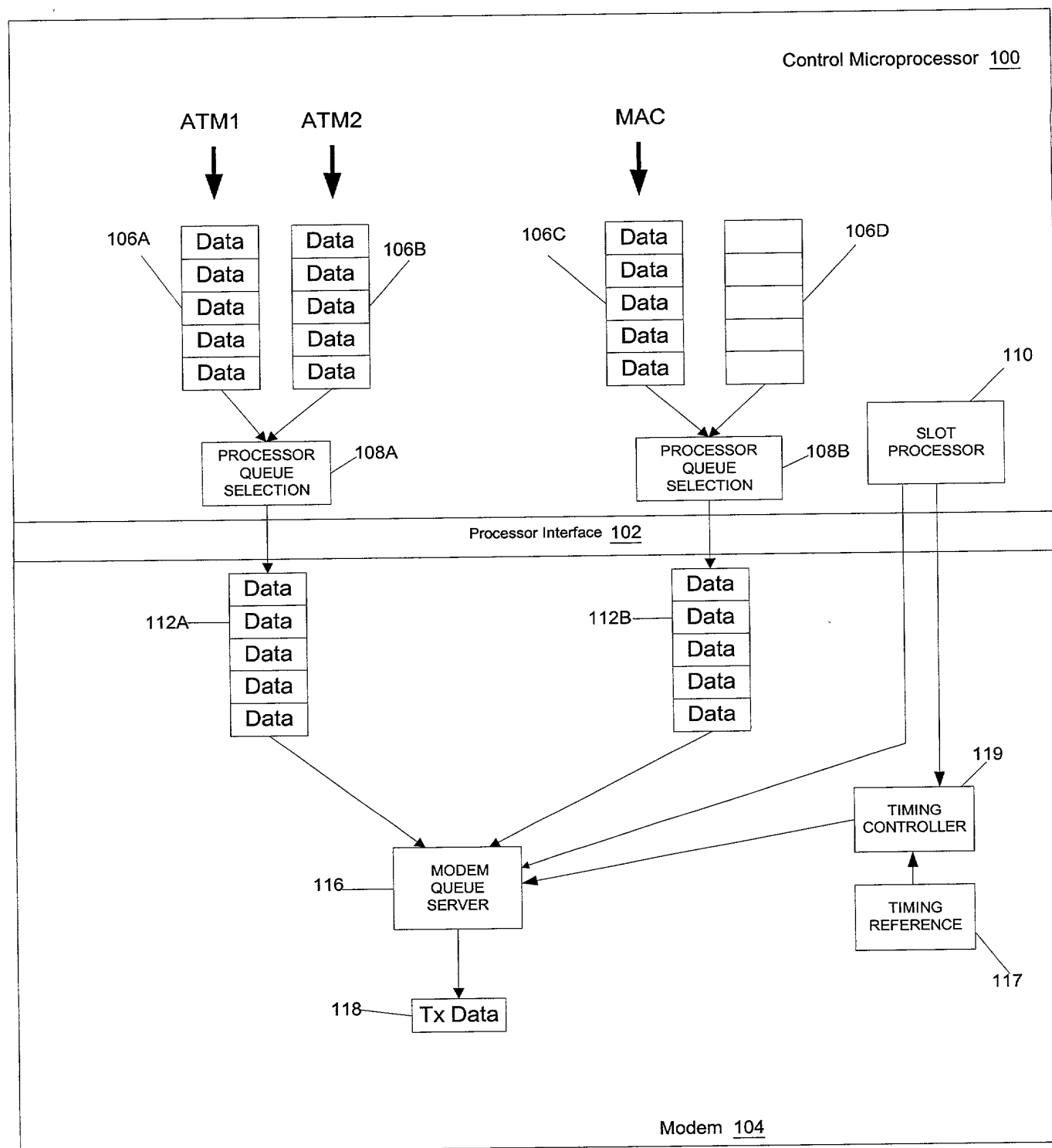


Figure 5

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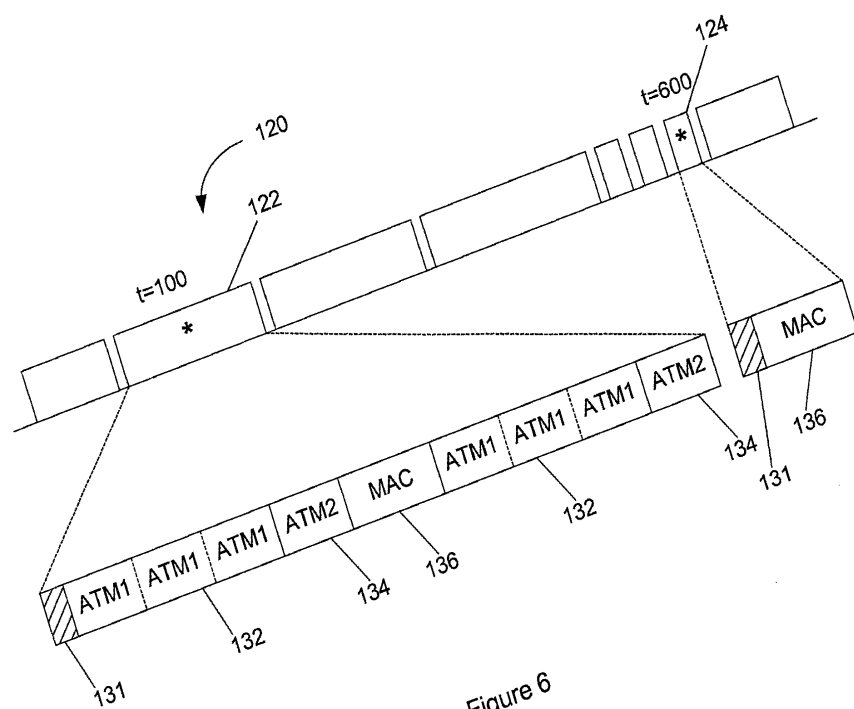


Figure 6

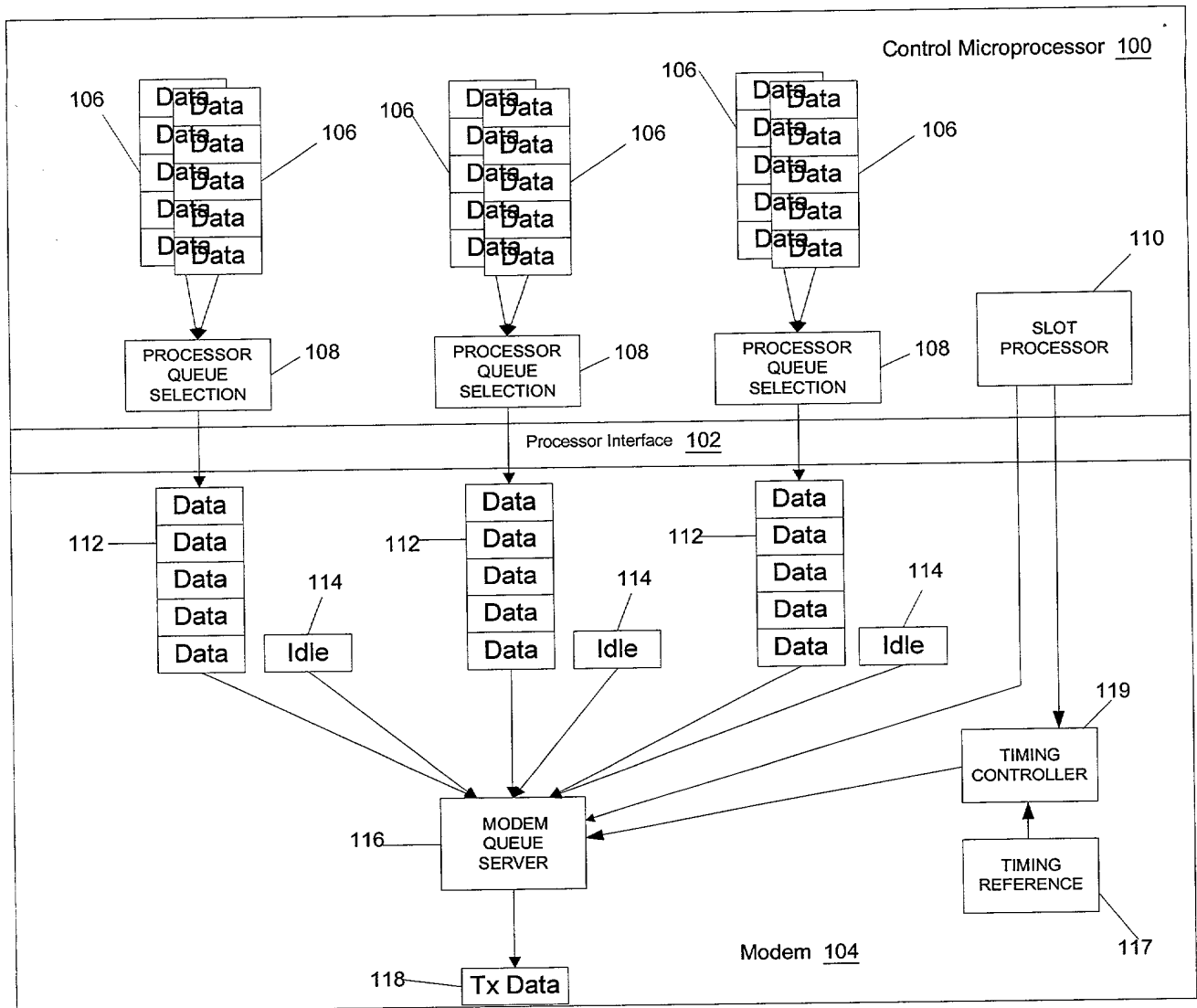


Figure 7